深圳市海天雄电子有限公司
Shenzhen Haitianxiong Electronic Co., Ltd.

CES-AM335X-CORE (Pin Type)

Product Spec.

TI ARM Mother Board

Rev. V1.0

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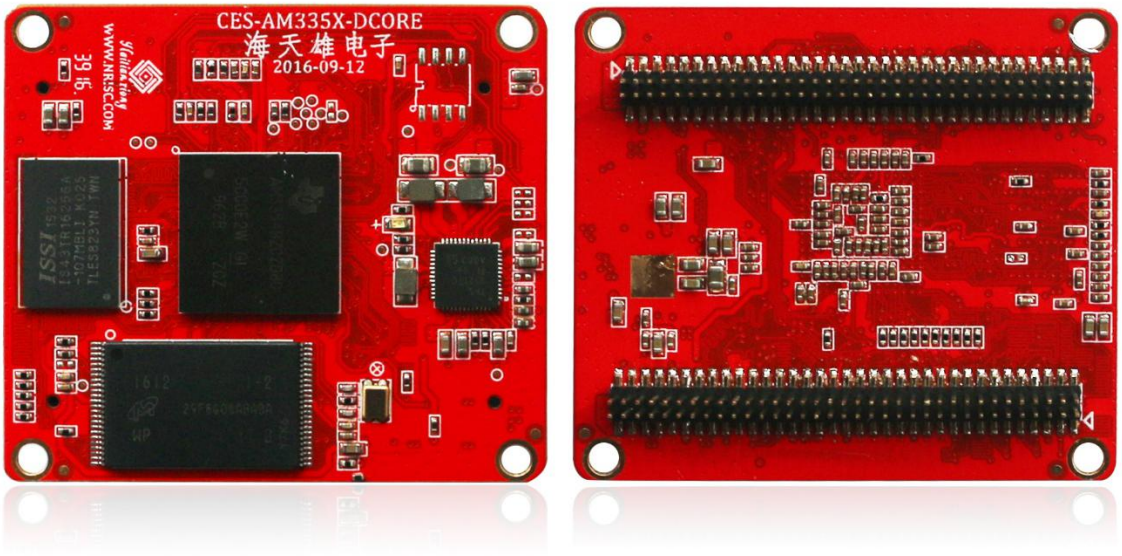


Introduction

CES-AM335X-CORE is an industrial Core Board based on TI Sitara™ ARM Cortex™-A8 AM3354 32bit RISC application processor, On-board 512MB DDR3, 1GB SLC Nand Flash and power management chip. The AM3354 provides PowerVR SGX™ Graphics Accelerator, 3D Graphics accelerator, industrial Communication Subsystem, and Programmable Real-Time Unit Subsystem.

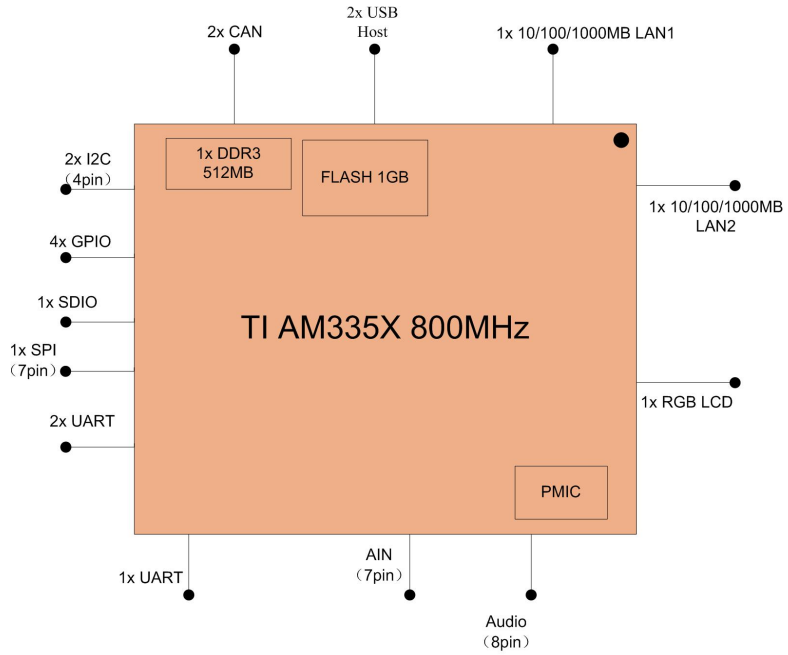
CES-AM335X-CORE can provides rich I/O such as 2x 10/100/1000MB LAN , USB Host/Otg,UART,RS485,SPI,I2C, CAN, GPIO, PRU-ICSS, SDIO, ADC, Audio and other .It can fulfill various applications indifferent industries, including self-service terminals, industrial automation, automotive electronics, digital signage, IOT, medical electronics, intelligent equipment, game, etc.

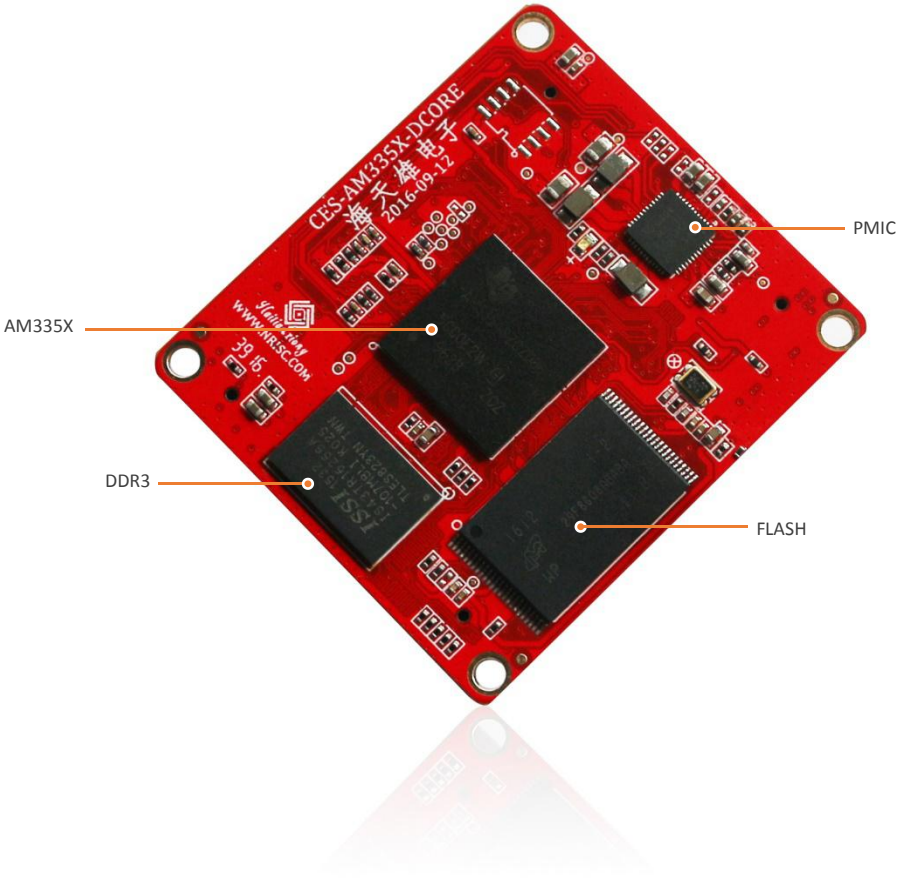
CES-AM335X-CORE provides corresponding development kit CES-335X for evaluation and test of the core board. Besides, the development kit CES-4418 also provides Android/Linux BSP package, schematic diagram, and users' manual for development, which are helpful for both software development and hardware design.

Feature

- TI Sitara™ ARM® Cortex®-A8 AM3354 1GHz 32- Bit RISC Processor
- Onboard DDR3 memory 512MB and 1GB SLC NANDFLASH
- Supports industrial interface options such as EtherCAT and PROFIBUS
- POWERVR SGX530，Industry-standard API support – Direct3D Mobile, OpenGL ES 1.1 and 2.0, OpenVG v1.0.1
- Provides 3D graphics acceleration
- Support LVDS、24bit RGB
- Two industrial Gigabit Ethernet MAC (10/100 / 1000Mbps)
- Rich I/O，such as CAN、MMC、SDIO、eQEP、eDMA、UART, GbE LAN, USB 2.0, GPIO, I2C, SPI
- Lowest power consumption
- Support Android4.2，Embedded Linux3.2

Block Diagram





Specification

Processor	
CPU	TI AM335X Cortex-A8 processor , AM3354BZCZD80 , 324Pin , NFBGA Package , Operating temperature range : –40°C~90°C
Architecture	ARMv7 Cortex-A8
Frequency	800MHz
Memory	
Capacity	512MB DDR3 IS43TR16256A , 96-ball FBGA , Operating temperature range : –40°C~95°C
Flash	1GB SLC NANDFLASH MT29F8G08ABABA , Operating temperature range : –40°C~85°C
PMIC	
Chipset	TPS65217C , Rated operating temperature range : –40°C~+105°C , Suitable for industrial applications
B2B Connectors	
Connectors Type	2* 2*35 1.27mm SMD
Pin Numbers	140pins
Pin Function	GPIO、RS485、LAN、LVDS/RGB、CAN、CSI、I2C、Audio、UART、SPI、JTAG、USB HOST/OTG
B2B Fixed Hole	
Fixed Hole	NO
Operation System	
OS	Optional Android 4.2、Embedded Linux 3.2
Physical	
Dimension (mm)	50*45mm

Pin List

J1				
Pin NO.	Signal Name	AM335X Ball Number	AM335X Ball Name	Remark
J1-1	VDD_5V		+5V	
J1-2	DGND		DGND	
J1-3	VDD_5V		+5V	
J1-4	VRTC	D7	VDDS_RTC	
J1-5	DGND		DGND	
J1-6	DGND		DGND	

J1-7	I2C0_SDA	C17	I2C0_SDA/TIMER4/UART2_CTSN/ECAP2_IN_PWM 2_OUT////GPIO3_5	I2C
J1-8	I2C1_SDA	B16	SPI0_D1/MMC1_SDWP/I2C1_SDA/EHRPWM0_TRI PZONE_INPUT/PR1_UART0_RXD/PR1_EDIO_DAT A_IN0/PR1_EDIO_DATA_OUT0/GPIO0_4	I2C
J1-9	I2C0_SCL	C16	I2C0_SCL/TIMER7/UART2_RTSN/ECAP1_IN_PWM 1_OUT////GPIO3_6	I2C
J1-10	I2C1_SCL	A16	SPI0_CS0/MMC2_SDWP/I2C1_SCL/EHRPWM0_SY NCI_O/PR1_UART0_TXD/PR1_E DIO_ DATA_ IN1/PR1_EDIO_ DATA_OUT1/GPIO0_5	I2C
J1-11	GPIO0_29	H18	RMII1_REFCLK/XDMA_EVENT_INTR2/SPI1_CS0/U ART5_TXD/MCASP1_AXR3/MMC0_POW/MCASP1 _AHCLKX/GPIO0_29	GPIO
J1-12	GPIO0_31	U17	GPMC_WPN/GMII2_RXERR/GPMC_CSN5/RMII2_R XERR/MMC2_SDCD/PR1_MDIO_M DCLK/ UART4_TXD/GPIO0 _31	GPIO
J1-13	GPIO1_31	V9	GPMC_CSN2/GPMC_BE1N/MMC1_CMD/PR1_EDI O_DATA_IN7/PR1_EDIO_DATA_OU T7/PR1_ PRU1_PRU_R30 _13/PR1_PRU1_PRU_R31_13/ GPIO1_31	GPIO
J1-14	GPIO1_28	U18	GPMC_BE1N/GMII2_COL/GPMC_CSN6/MMC2_DA T3/GPMC_DIR/PR1_MII1_RXLINK/MCASP0_ACLK R/GPIO1_28	GPIO
J1-15	MMC0_DAT2	F18	MMC0_DAT2/GPMC_A21/UART4_RTSN/TIMER6/U ART1_DSRN/PR1_PRU0_PRU_R30_9/PR1_PRU0_ PRU_R31_9/GPIO2_27	SD
J1-16	MMC0_DAT3	F17	MMC0_DAT3/GPMC_A20/UART4_CTSN/TIMER5/U ART1_DCDN/PR1_PRU0_PRU_R30_8/PR1_PRU0_ PRU_R31_8/GPIO2_26	SD
J1-17	MMC0_DAT0	G16	MMC0_DAT0/GPMC_A23/UART5_RTSN/UART3_T XD/UART1_RIN/PR1_PRU0_PRU_R30_11/PR1_PR U0_PRU_R31_11/GPIO2_29	SD
J1-18	MMC0_DAT1	G15	MMC0_DAT1/GPMC_A22/UART5_CTSN/UART3_R XD/UART1_DTRN/PR1_PRU0_PRU_R30_10/PR1_ PRU0_PRU_R31_10/GPIO2_28	SD
J1-19	MMC0_CMD	G18	MMC0_CMD/GPMC_A25/UART3_RTSN/UART2_TX D/DCAN1_RX/PR1_PRU0_PRU_R30_13/PR1 _PRU0_PRU_ R31_13/GPIO2_31	SD

J1-20	MMC0_CLKO	G17	MMC0_CLK/GPMC_A24/UART3_CTSN/UART2_RXD/DCAN1_TX/PR1_PRU0_PRU_R30_12/PR1_PRU0_PRU_R31_12/GPIO2_30	SD
J1-21	CD/EMU4	C15	SPI0_CS1/UART3_RXD/ECAP1_IN_PWM1_OUT/MMC0_POW/XDMA_EVENT_INTR2/MMC0_SDCD/EMU4/GPIO0_6	SD
J1-22	SYS_RESETn	A10	NRESET_INOUT	RESET
J1-23	DGND		DGND	
J1-24	GPIO3_0	H16	GMII1_COL/RMII2_REFCLK/SPI1_SCLK/UART5_RXD/MCASP1_AXR2/MMC2_DAT 3/MCASP0_AXR2/GPIO3_0	SPI
J1-25	CLKOUT2	D14	EVENT_INTR1/TCLKIN/CLKOUT2/TIMER7/PR1PRU0_PRUR31_16/EMU3/GPIO0_20	
J1-26	GPIO3_1	H17	GMII1_CRS/RMII1_CRS_DV/SPI1_D0/I2C1_SDA/MCASP1_ACLKX/UART5_CTSN/UART2_RXD/GPIO3_1	SPI
J1-27	CLKOUT1	A15	EVENT_INTR0/TIMER4/CLKOUT1/SPI1_CS1/PR1PRU1R31_16/EMU2/GPIO0_19	
J1-28	GPIO3_2	J15	GMII1_RXERR/RMII1_RXERR/SPI1_D1/I2C1_SCL/MCASP1_FSX/UART5_RTSN/ UART2_TXD/ GPIO3_2	SPI
J1-29	DGND		DGND	
J1-30	GPIO2_0	T13	GPMC_CSN3/MMC2_CMD/PR1_MDIO_DATA/GPIO2_0	SPI
J1-31	GPIO1_30	U9	GPMC_CSN1/GPMC_CLK/MMC1_CLK/PRT1EDIO_DATA_IN6/PRT1_EDIO_DATA_OUT6/PR1_PRU1_PRU_R30_12/PR1_PRU1_PRU_R31_12/GPIO1_30	SPI
J1-32	GPIO2_1	V12	GPMC_CLK/LCD_MEM_CLK/GPMC_WAIT1/MMC2_CLK/PRT1_MII1_TXEN/MCASP0_FSR/GPIO2_1	SPI
J1-33	GPIO0_7	C18	ECAP0_IN_PWM0_OUT/UART3_TXD/SPI1_CS1/PR1_ECAP0_ECAP_CAPIN_APWM_O/SPI1_SCLK/MMC0_SDWP/XDMA_EVENT_INTR2/GPIO0_7	SPI
J1-34	DGND		DGND	
J1-35	UART0_TXD	E16	UART0_TXD/SPI1_CS1/DCAN0_RX/I2C2_SCL/ECAP1_IN_PWM1_OUT/PR1_PRU1_PRU_R30_15/PR1_PRU1_PRU_R31_15/GPIO1_11	UART

J1-36	UART1_TXD	D15	UART1_TXD/MMC2_SDWP/DCAN1_RX/I2C1_SCL//PR1_UART0_TXD/PR1_PRU0_PRU_R31_16/GPIO0_15	UART
J1-37	UART0_RXD	E15	UART0_RXD/SPI1_CS0/DCAN0_TX/I2C2_SDA/ECAP2_IN_PWM2_OUT/PR1_PRU1_PRU_R30_14/PR1_PRU1_PRU_R31_14/GPIO1_10	UART
J1-38	UART1_RXD	D16	UART1_RXD/MMC1_SDWP/DCAN1_TX/I2C1_SDA//PR1_UART0_RXD/PR1_PRU1_PRU_R31_16/GPIO0_14	UART
J1-39	UART2_TXD	B17	SPI0_D0/UART2_TXD/I2C2_SCL/EHRPWM0B/PR1_UART0_RTS_N/PR1_EDIO_LA TCH_IN/EMU3/GPIO0_3	UART
J1-40	DGND		DGND	
J1-41	UART2_RXD	A17	SPI0_SCLK/UART2_RXD/I2C2_SDA/EHRPWM0A/PR1_UART0_CTS_N/PR1_EDIO_SOF/EMU2/GPIO0_2	UART
J1-42	DGND		DGND	
J1-43	VDD_ADC	B9	+1.8V	
J1-44	GNDA_ADC	E8	VSSA_ADC	DGND
J1-45	AIN0	B6	AIN0	AIN
J1-46	AIN1	C7	AIN1	AIN
J1-47	AIN2	B7	AIN2	AIN
J1-48	AIN3	A7	AIN3	AIN
J1-49	AIN4	C8	AIN4	AIN
J1-50	AIN5	B8	AIN5	AIN
J1-51	AIN6	A8	AIN6	AIN
J1-52	GNDA_ADC	E8	VSSA_ADC	DGND
J1-53	MCASP0_FSR	C13	MCASP0_FSR/EQEP0B_IN/MCASP0_AXR3/MCASP1_FSX/EMU2/PR1_PRU0_PRU_R30_5/PR1_PRU0_PRU_R31_5/GPIO3_19	AUDIO

J1-54	MCASP0_AHCLKX	A14	MCASP0_AHCLKX/EQEP0_STROBE/MCASP0_AXR3/MCASP1_AXR1/EMU4/PR1_PRU0_PRU_R30_7/PR1_PRU0_PRU_R31_7/GPIO3_21	AUDIO
J1-55	MCASP0_ACLKX	A13	MCASP0_ACLKX/EHRPWM0A//SPI1_SCLK/MMC0_SDCD/PR1_PRU0_PRU_R30_0/PR1_PRU0_PRU_R31_0/GPIO3_14	
J1-56	MCASP0_FSX	B13	MCASP0_FSX/EHRPWM0B//SPI1_D0/MMC1_SDCD/PR1_PRU0_PRU_R30_1/PR1_P RU0_PRU_R31_1/GPIO3_15	AUDIO
J1-57	MCASP0_AHCLKR	C12	MCASP0_AHCLKR/EHRPWM0_SYNCI_O/MCASP0_AXR2/SPI1_CS0/ECAP2_IN_PWM2_OUT/PR1_PRU0_PRU_R30_3/PR1_PRU0_PRU_R31_3/GPIO3_17	AUDIO
J1-58	MCASP0_AXR1	D13	MCASP0_AXR1/EQEP0_INDEX//MCASP1_AXR0/EMU3/PR1_PRU0_PRU_R30_6/PR1_PRU0_PRU_R31_6/GPIO3_20	AUDIO
J1-59	MCASP0_AXR0	D12	MCASP0_AXR0/EHRPWM0_TRIPZONE_INPUT//SPI1_D1/MMC2_SDCD/PR1_PRU0_PRU_R30_2/PR1_PRU0_PRU_R31_2/GPIO3_16	AUDIO
J1-60	MCASP0_ACLKR	B12	MCASP0_ACLKR/EQEP0A_IN/MCASP0_AXR2/MCASP1_ACLKX/MMC0_SDWP/PR1_PRU0_ PRU_R30_4/PR1_PRU0_PRU_R31_4/GPIO3_18	AUDIO
J1-61	DGND		DGND	
J1-62	DGND		DGND	
J1-63	GPIO2_10	T1	LCD_DATA4/GPMC_A4//EQEP2A_IN//PR1_PRU1_PRU_R30_4/PR1_PRU1_PRU_R31_4/GPIO2_10	LCD
J1-64	GPIO2_9	R4	LCD_DATA3/GPMC_A3//EHRPWM2_SYNCI_O//PR1_PRU1_PRU_R30_3/PR1_PRU1 _PRU_R31_3/GPIO2_9	LCD
J1-65	GPIO2_8	R3	LCD_DATA2/GPMC_A2//EHRPWM2_TRIPZONE_INPUT//PR1_PRU1_PRU_R30_2/PR1_PRU1_PRU_R31_2/GPIO2_8	LCD
J1-66	GPIO2_7	R2	LCD_DATA1/GPMC_A1//EHRPWM2B//PR1_PRU1_PRU_R30_1/PR1_PRU1_PRU_R31_1/GPIO2_7	LCD
J1-67	GPIO2_6	R1	LCD_DATA0/GPMC_A0//EHRPWM2A//PR1_PRU1_PRU_R30_0/PR1_PRU1_PRU_R31_0/GPIO2_6	LCD

J1-68	GPIO1_15	U13	GPMC_AD15/LCD_DATA16/MMC1_DAT7/MMC2_DAT3/EQEP2_STROBE/PR1_ECAP0_ECAP_CAPIN_APWM_O/PR1_PRU0_PRU_R31_15/GPIO1_15	LCD
J1-69	GPIO1_13	R12	GPMC_AD13/LCD_DATA18/MMC1_DAT5/MMC2_DAT1/EQEP2B_IN/PR1_MII0_TXD1/PR1_PRU0_PRU_R30_15/GPIO1_13	LCD
J1-70	GPIO0_26	T11	GPMC_AD10/LCD_DATA21/MMC1_DAT2/MMC2_DAT6/EHRPWM2_TRIPZONE_INPUT/PR1_MII0_TXEN//GPIO0_26	LCD

J2				
Pin NO.	Signal Name	AM335X Ball Number	AM335X Ball Name	Remark
J2-1	UART3_CTSN	U3	LCD_DATA10/GPMC_A14/EHRPWM1A/MCASP0_AXR0//PR1_MII0_RXD1/UART3_CTSN/GPIO 2_16	LCD
J2-2	UART5_RXD	U2	LCD_DATA9/GPMC_A13/EHRPWM1_SYNCI_O/MCASP0_FSX/UART5_RXD/PR1_MII0_RXD2/UART2_RTSN/GPIO2_15	LCD
J2-3	UART5_TXD	U1	LCD_DATA8/GPMC_A12/EHRPWM1_TRIPZONE_INPUT/MCASP0_ACLKX/UART5_TXD/PR1_MII0_RXD3/UART2_CTSN/GPIO2_14	LCD
J2-4	GPIO2_13	T4	LCD_DATA7/GPMC_A7/PR1_EDIO_DATA_IN7/EQEP2_STROBE/PR1_EDIO_DATA_OUT7/PR1_PRU1_PRU_R30_7/PR1_PRU1_PRU_R31_7/GPIO2_13	LCD
J2-5	GPIO2_12	T3	LCD_DATA6/GPMC_A6/PR1_EDIO_DATA_IN6/EQEP2_INDEX/PR1_EDIO_DATA_OUT6/PR1_PRU1_PRU_R30_6/PR1_PRU1_PRU_R31_6/GPIO2_12	LCD
J2-6	GPIO2_11	T2	LCD_DATA5/GPMC_A5//EQEP2B_IN//PR1_PRU1_PRU_R30_5/PR1_PRU1_PRU_R31_5/GPIO2_11	LCD
J2-7	GPIO1_12	T12	GPMC_AD12/LCD_DATA19/MMC1_DAT4/MMC2_DAT0/EQEP2A_IN/PR1_MII0_TXD2/PR1_PRU0_PRU_R30_14/GPIO1_12	LCD
J2-8	EHRPWM2B	T10	GPMC_AD9/LCD_DATA22/MMC1_DAT1/MMC2_DAT5/EHRPWM2B/PR1_MII0_CRS// GPIO0_23	LCD

J2-9	UART5_RTSN	T5	LCD_DATA15/GPMC_A19/EQEP1_STROBE/MCASP0_AHCLKX/MCASP0_AXR3/PR1_MII0_RXDV/UART5_RTSN/GPIO0_11	LCD
J2-10	UART5_CTSN	V4	LCD_DATA14/GPMC_A18/EQEP1_INDEX/MCASP0_AXR1/UART5_RXD/PR1_MII_MR0_CLK/UART5_CTSN/GPIO0_10	LCD
J2-11	UART4_RTSN	V3	LCD_DATA13/GPMC_A17/EQEP1B_IN/MCASP0_FSR/MCASP0_AXR3/PR1_MII0_RXER/UART4_RTSN/GPIO0_9	LCD
J2-12	UART4_CTSN	V2	LCD_DATA12/GPMC_A16/EQEP1A_IN/MCASP0_ACLKR/MCASP0_AXR2/PR1_MII0_RXLINK/UART4_CTSN/GPIO0_8	LCD
J2-13	UART3_RTSN	U4	LCD_DATA11/GPMC_A15/EHRPWM1B/MCASP0_AHCLKR/MCASP0_AXR2/PR1_MII0_RXD0/UART3_RTSN/GPIO2_17	LCD
J2-14	GPIO1_14	V13	GPMC_AD14/LCD_DATA17/MMC1_DAT6/MMC2_DAT2/EQEP2_INDEX/PR1_MII0_TXD0/PR1_PRU0_PRU_R31_14/GPIO1_14	LCD
J2-15	GPIO0_27	U12	GPMC_AD11/LCD_DATA20/MMC1_DAT3/MMC2_DAT7/EHRPWM2_SYNCI_O/PR1_MII0_TXD3//GPIO0_27	LCD
J2-16	EHRPWM2A	U10	GPMC_AD8/LCD_DATA23/MMC1_DAT0/MMC2_DAT4/EHRPWM2A/PR1_MII_MT0_CLK//GPIO0_22	LCD
J2-17	GPIO2_25	R6	LCD_AC_BIAS_EN/GPMC_A11//PR1_EDIO_DATA_IN5/PR1_EDIO_DATA_OUT5/PR1_PRU1_PRU_R30_11/PR1_PRU1_PRU_R31_11/GPIO2_25	LCD
J2-18	GPIO2_23	R5	LCD_HSYNC/GPMC_A9//PR1_EDIO_DATA_IN3/PR1_EDIO_DATA_OUT3/PR1_PRU1_PRU_R30_9/PR1_PRU1_PRU_R31_9/GPIO2_23	LCD
J2-19	GPIO2_22	U5	LCD_VSYNC/GPMC_A8//PR1_EDIO_DATA_IN2/PR1_EDIO_DATA_OUT2/PR1_PRU1_PRU_R30_8/PR1_PRU1_PRU_R31_8/GPIO2_22	LCD
J2-20	GPIO2_24	V5	LCD_PCLK/GPMC_A10//PR1_EDIO_DATA_IN4/PR1_EDIO_DATA_OUT4/PR1_PRU1_PRU_R30_10/PR1_PRU1_PRU_R31_10/GPIO2_24	LCD
J2-21	DGND		DGND	
J2-22	DGND		DGND	

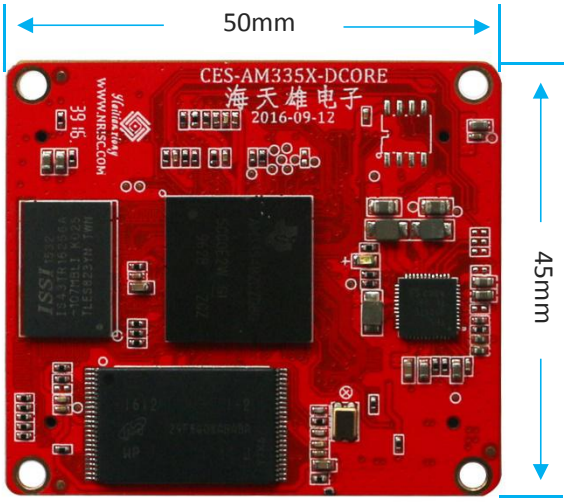
J2-23	RGMII2_RCTL	V14	GPMC_A1/GMII2_RXDV/RGMII2_RCTL/MMC2_DAT0/GPMC_A17/PR1_MII1_TXD3/EHRPWM1_SYNCI_O/GPIO1_17	LAN2
J2-24	RGMII2_RXD3	V16	GPMC_A8/GMII2_RXD3/RGMII2_RD3/MMC2_DAT6/GPMC_A24/PR1_MII1_RXD0/MCASP0_ACLKX/GPIO1_24	LAN2
J2-25	RGMII2_RXD2	U16	GPMC_A9/GMII2_RXD2/RGMII2_RD2/MMC2_DAT7/GPMC_A25/PR1_MII_MR1_CLK/MCASP0_FSX/GPIO1_25	LAN2
J2-26	RGMII2_RXD1	T16	GPMC_A10/GMII2_RXD1/RGMII2_RD1/RMII2_RXD1/GPMC_A26/PR1_MII1_CRS/MCASP0_AXR0/GPIO1_26	LAN2
J2-27	RGMII2_RXD0	V17	GPMC_A11/GMII2_RXD0/RGMII2_RD0/RMII2_RXD0/GPMC_A27/PR1_MII1_RXER/MCASP0_AXR1/GPIO1_27	LAN2
J2-28	RGMII2_RCLK	T15	GPMC_A7/GMII2_RXCLK/RGMII2_RCLK/MMC2_DAT5/GPMC_A23/PR1_MII1_RXD1/EQEP1_STROBE/GPIO1_23	LAN2
J2-29	RGMII2_TCLK	U15	GPMC_A6/GMII2_TXCLK/RGMII2_TCLK/MMC2_DAT4/GPMC_A22/PR1_MII1_RXD2/EQEP1_INDEX/GPIO1_22	LAN2
J2-30	RGMII2_TXD3	U14	GPMC_A2/GMII2_TXD3/RGMII2_TD3/MMC2_DAT1/GPMC_A18/PR1_MII1_TXD2/EHRPWM1A/GPIO1_18	LAN2
J2-31	RGMII2_TXD2	T14	GPMC_A3/GMII2_TXD2/RGMII2_TD2/MMC2_DAT2/GPMC_A19/PR1_MII1_TXD1/EHRPWM1B/GPIO1_19	LAN2
J2-32	RGMII2_TXD1	R14	GPMC_A4/GMII2_TXD1/RGMII2_TD1/RMII2_TXD1/GPMC_A20/PR1_MII1_TXD0/EQEP1A_IN/GPIO1_20	LAN2
J2-33	RGMII2_TXD0	V15	GPMC_A5/GMII2_TXD0/RGMII2_TD0/RMII2_TXD0/GPMC_A21/PR1_MII1_RXD3/EQEP1B_IN/GPIO1_21	LAN2
J2-34	RGMII2_TCTL	R13	GPMC_A0/GMII2_TXEN/RGMII2_TCTL/RMII2_TXEN/GPMC_A16/PR1_MII_MT1_CLK/EHRPWM1_TRIPZONE_INPUT/GPIO1_16	LAN2
J2-35	MDIO_DATA	M17	MDIO_DATA/TIMER6/UART5_RXD/UART3_CTSN/MMC0_SDCD/MMC1_CMD/MMC2_CMD/GPIO0_0	LAN2

J2-36	MDIO_CLK	M18	MDIO_CLK/TIMER5/UART5_TXD/UART3_RTSN/MMC0_SDWP/MMC1_CLK/MMC2_CLK/GPIO0_1	LAN2
J2-37	DGND		DGND	
J2-38	DGND		DGND	
J2-39	RGMII1_RXD3	L17	GMII1_RXD3/UART3_RXD/RGMII1_RD3/MMC0_DAT5/MMC1_DAT2/UART1_DTRN/MCASP0_AXR0/GPIO2_18	LAN1
J2-40	RGMII1_RCTL	J17	GMII1_RXDV/LCD_MEMORY_CLK/RGMII1_RCTL/UART5_TXD/MCASP1_ACLKX/MMC2_DAT0/MCASP0_ACLKR/GPIO3_4	LAN1
J2-41	RGMII1_RXD1	L15	GMII1_RXD1/RMII1_RXD1/RGMII1_RD1/MCASP1_AXR3/MCASP1_FSR/EQEP0_STROBE/MMC2_CLK/GPIO2_20	LAN1
J2-42	RGMII1_RXD2	L16	GMII1_RXD2/UART3_TXD/RGMII1_RD2/MMC0_DAT4/MMC1_DAT3/UART1_RIN/MCASP0_AXR1/GPIO2_19	LAN1
J2-43	RGMII1_RCLK	L18	GMII1_RXCLK/UART2_TXD/RGMII1_RCLK/MMC0_DAT6/MMC1_DAT1/UART1_DSRN/MCASP0_FSX/GPIO3_10	LAN1
J2-44	RGMII1_RXD0	M16	GMII1_RXD0/RMII1_RXD0/RGMII1_RD0/MCASP1_AHCLKX/MCASP1_AHCLKR/MCASP1_ACLKR/MCASP0_AXR3/GPIO2_21	LAN1
J2-45	RGMII1_TXD3	J18	GMII1_TXD3/DCAN0_TX/RGMII1_TD3/UART4_RXD/MCASP1_FSX/MMC2_DAT1/MCASP0_FSR/GPIO0_16	LAN1
J2-46	RGMII1_TCTL	J16	GMII1_TXEN/RMII1_TXEN/RGMII1_TCTL/TIMER4/MCASP1_AXR0/EQEP0_INDEX/MMC2_CMD/GPIO3_3	LAN1
J2-47	RGMII1_TXD1	K16	GMII1_TXD1/RMII1_TXD1/RGMII1_TD1/MCASP1_FSR/MCASP1_AXR1/EQEP0A_IN/MMC1_CMD/GPIO0_21	LAN1
J2-48	RGMII1_TXD2	K15	GMII1_TXD2/DCAN0_RX/RGMII1_TD2/UART4_TXD/MCASP1_AXR0/MMC2_DAT2/MCASP0_AHCLKX/GPIO0_17	LAN1
J2-49	RGMII1_TCLK	K18	GMII1_TXCLK/UART2_RXD/RGMII1_TCLK/MMC0_DAT7/MMC1_DAT0/UART1_DCDN/MCASP0_ACLKX/GPIO3_9	LAN1

J2-50	RGMII1_TXD0	K17	GMII1_TXD0/RMII1_TXD0/RGMII1_TD0/MCASP1_AXR2/MCASP1_ACLKR/EQEP0B_IN/MMC1_CLK/GPIO0_28	LAN1
J2-51	DGND		DGND	
J2-52	DGND		DGND	
J2-53	USB0_ID	P16	USB0_ID	USB0
J2-54	USB1_ID	P17	USB1_ID	USB1
J2-55	USB0_DM	N18	USB0_DM	USB0
J2-56	USB1_DM	R18	USB1_DM	USB1
J2-57	USB0_DP	N17	USB0_DP	USB0
J2-58	USB1_DP	R17	USB1_DP	USB1
J2-59	USB_DC	P17	USB0_VBUS	USB0
J2-60	USB1_VBUS	T18	USB1_VBUS	USB1
J2-61	DGND		DGND	
J2-62	USB1_DRVVBUS	F15	USB1_DRVVBUS/GPIO3_13	USB1
J2-63	CAN0_RX	D17	UART1_RTSN/TIMER5/DCAN0_RX/I2C2_SCL/SPI1_CS1/PR1_UART0_RTS_N/PR1_EDC_LATCH1_IN/GPIO0_13	CAN0
J2-64	CAN1_RX	E17	UART0_RTSN/UART4_TXD/DCAN1_RX/I2C1_SCL/SPI1_D1/SPI1_CS0/PR1_EDC_SYNC1_OUT/GPIO1_9	CAN1
J2-65	CAN0_TX	D18	UART1_CTSN/TIMER6/DCAN0_TX/I2C2_SDA/SPI1_CS0/PR1_UART0_CTS_N/PR1_EDC_LATCH0_IN/GPIO0_12	CAN0
J2-66	CAN1_TX	E18	UART0_CTSN/UART4_RXD/DCAN1_TX/I2C1_SDA/SPI1_D0/TIMER7/PR1_EDC_SYNC0_OUT/GPIO1_8	CAN1
J2-67	DGND		DGND	
J2-68	DGND		DGND	

J2-69	VDD_5V		+5V	
J2-70	DGND		DGND	

Dimensions



Ordering Information

Part No.	CPU	Memory	Flash Memory	UART	USB	CAN	GbE LAN	Display	Power	Operating Temp.
CES-AM335X-CORE	TI AM3354 1GHz	512MB	1GB	6	2	1	2	LVDS/RGB	5V	-20~70℃

Development Board

Part No.	Description
CES-AM335X	CES-AM335X Development Boart/Kit

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Statement

This introduction Information is only for users' reference，without prior notice for the necessary amendment.

More product information, please visit www.nrisc.com

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